Taiwan Semiconductor

# 2A, 200V - 1000V Standard Surface Mount Rectifier

# FEATURES

- Glass passivated chip junction
- Ideal for automated placement
- Low reverse leakage
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

## APPLICATIONS

- DC to DC converter
- Switching mode converters and inverters
- General purpose

# **MECHANICAL DATA**

- Case: SOD-123FL
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 1 whisker test
- Polarity: Indicated by cathode band
- Weight: 0.016g (approximately)

KEY PARAMETERS			
PARAMETER	VALUE	UNIT	
I <sub>F</sub>	2	А	
V <sub>RRM</sub>	200 - 1000	V	
I <sub>FSM</sub>	40	А	
T <sub>J MAX</sub>	150	°C	
Package	SOD-123FL		
Configuration	Single die		

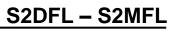




SOD-123FL



ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C unless otherwise noted)								
PARAMETER		SYMBOL	S2DFL	S2GFL	S2JFL	S2KFL	S2MFL	UNIT
Marking code on the devi	се		S2DF	S2GF	S2JF	S2KF	S2MF	
Repetitive peak reverse voltage		V <sub>RRM</sub>	200	400	600	800	1000	V
Reverse voltage, total rm	s value	V <sub>R(RMS)</sub>	140	280	420	560	700	V
Forward current		I <sub>F</sub>			2			Α
Surge peak forward current, single half sine-	t = 8.3ms	I			40			А
wave superimposed on rated load	t = 1.0ms	I <sub>FSM</sub>			100			А
Junction temperature		TJ			-55 to +150	)		°C
Storage temperature		T <sub>STG</sub>			-55 to +150	)		°C





THERMAL PERFORMANCE				
PARAMETER	SYMBOL	ТҮР	UNIT	
Junction-to-lead thermal resistance	R <sub>ejl</sub>	81	°C/W	
Junction-to-ambient thermal resistance	R <sub>θJA</sub>	116	°C/W	
Junction-to-case thermal resistance	R <sub>eJC</sub>	69	°C/W	

Thermal Performance Note: Units mounted on PCB (5mm x 5mm Cu pad test board)

<b>ELECTRICAL SPECIFICATIONS</b> ( $T_A = 25^{\circ}C$ unless otherwise noted)					
PARAMETER	CONDITIONS	SYMBOL	ТҮР	MAX	UNIT
Forward voltage <sup>(1)</sup>	$I_F = 1A, T_J = 25^{\circ}C$		0.90	-	V
	$I_F = 2A, T_J = 25^{\circ}C$	V <sub>F</sub>	0.97	1.10	V
	$I_F = 1A, T_J = 125^{\circ}C$		0.81	-	V
	$I_F = 2A, T_J = 125^{\circ}C$		0.90	1.00	V
Reverse current @ rated $V_R^{(2)}$	$T_J = 25^{\circ}C$	I	-	5	μA
	T <sub>J</sub> = 125°C	– I <sub>R</sub>	-	100	μA
Junction capacitance	1MHz, V <sub>R</sub> = 4.0V	CJ	10	-	pF

#### Notes:

1. Pulse test with PW = 0.3ms

2. Pulse test with PW = 30ms

# ORDERING INFORMATION

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ORDERING CODE <sup>(1)</sup>	PACKAGE	PACKING
S2xFL	SOD-123FL	10,000 / Tape & Reel

#### Notes:

1. "x" defines voltage from 200V(S2DFL) to 1000V(S2MFL)



1000

100

10

1

0.1

0.01

0.001

10

INSTANTANEOUS REVERSE CURRENT (µA)

### **CHARACTERISTICS CURVES**

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$ 

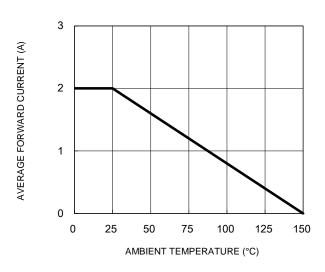
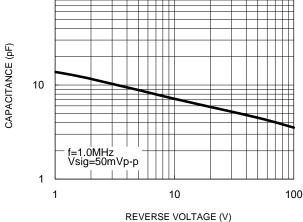


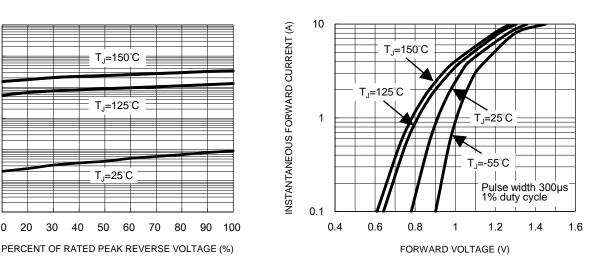
Fig.1 Forward Current Derating Curve

#### **Fig.3 Typical Reverse Characteristics**



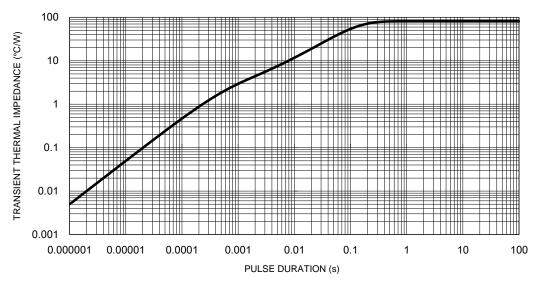
**Fig.2 Typical Junction Capacitance** 

**Fig.4 Typical Forward Characteristics** 



100



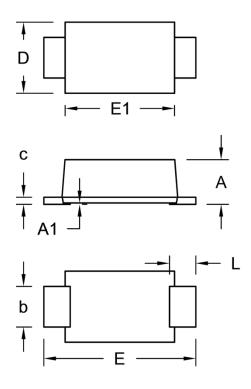


# S2DFL – S2MFL Taiwan Semiconductor



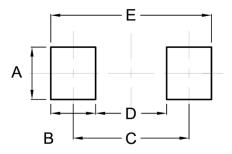
# **PACKAGE OUTLINE DIMENSIONS**

SOD-123FL



DIM.	Unit	Unit (mm)		(inch)
	Min.	Max.	Min.	Max.
A	1.00	1.20	0.039	0.047
A1	0.02	0.05	0.001	0.002
b	0.90	1.10	0.035	0.043
с	0.10	0.25	0.004	0.010
D	1.60	1.90	0.063	0.075
E	3.60	3.90	0.142	0.154
E1	2.55	2.85	0.100	0.112
L	0.40	0.90	0.016	0.035

# SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
A	1.40	0.055
В	1.20	0.047
С	3.10	0.122
D	1.90	0.075
E	4.30	0.169

#### **MARKING DIAGRAM**



P/N	= Marking Code
YW	= Date Code
F	= Factory Code



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